

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

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U.S. **UTILITY** Patent Application

<p><i>m.l.k</i> O.I.P.E. SCANNED <i>82</i> Q.A. <i>AA</i></p>	PATENT DATE
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73 Jc906

APPLICATION NO. 09/625116	CONT/PRIOR D F	CLASS 257 361	SUBCLASS 5C	ART UNIT 2811 2536	EXAMINER Boris Senarso
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Isabelle Claverie

TITLE

Protection of the logic well of a component including an integrated MOS power transistor

PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
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